| <del></del>  |  |  | Ñ                        |
|--|--|--|--------------------------|
|  | Application No.  | Applicant(s)                                 | <del></del>              |
| Notice of Allowability   | 10/607,610   | SHUM ET AL.                                  |                          |
| Notice of Allowability   | Examiner   | Art Unit                                     |                          |
|  | Tu-Tu Ho   | 2818   |                          |
| The MAILING DATE of this communication ap<br>All claims being allowable, PROSECUTION ON THE MERITS<br>herewith (or previously mailed), a Notice of Allowance (PTOL-INOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT<br>of the Office or upon petition by the applicant. See 37 CFR 1.3 | IS (OR REMAINS) CLOSED in this a<br>35) or other appropriate communicati<br>RIGHTS. This application is subject<br>313 and MPEP 1308.  | application. If not included                 | ie THIS<br>he initiative |
| 1. This communication is responsive to <u>Papers filed 06/27.</u>  | <u>/2003 and 02/23/2004</u> .  |  |                          |
| 2. The allowed claim(s) is/are <u>1-30</u> .   |  |  |                          |
| 3. The drawings filed on 23 February 2004 are accepted b   | y the Examiner.  |  |                          |
| <ul> <li>4. ☐ Acknowledgment is made of a claim for foreign priority</li> <li>a) ☐ All b) ☐ Some* c) ☐ None of the:</li> <li>1. ☐ Certified copies of the priority documents had</li> <li>2. ☐ Certified copies of the priority documents had</li> </ul>                               | ave been received.   |  |                          |
| 3. Copies of the certified copies of the priority  |  |  | om the                   |
| International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:   |  |  |                          |
| Applicant has THREE MONTHS FROM THE "MAILING DATI noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  | E" of this communication to file a repl<br>NMENT of this application.  | y complying with the requirem                | nents                    |
| 5. A SUBSTITUTE OATH OR DECLARATION must be sub<br>INFORMAL PATENT APPLICATION (PTO-152) which g   | omitted. Note the attached EXAMINE ives reason(s) why the oath or decla  | R'S AMENDMENT or NOTICE ration is deficient. | E OF                     |
| 6. CORRECTED DRAWINGS ( as "replacement sheets") m   | nust be submitted.   |  |                          |
| (a) ☐ including changes required by the Notice of Draftspe   |  | 0-948) attached                              |                          |
| 1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date   | <u> </u>   |  |                          |
| <ul> <li>(b) ☐ including changes required by the attached Examine<br/>Paper No./Mail Date</li> </ul>   | er's Amendment / Comment or in the   | Office action of                             | 1 .                      |
| Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in   | t 1.84(c)) should be written on the draw   | rings in the front (not the back)            | of ,                     |
| 7. DEPOSIT OF and/or INFORMATION about the department of DEPOSIT OF and/or INFORMATION about the department regarding REQUIREMEN   | oosit of BIOLOGICAL MATERIAL   | must be submitted. Note th                   | ne                       |
|  |  |  |                          |
|  |  |  | •                        |
| inang Panguaran Indonésia yang dia bahasa bahas<br>Bahasa Bahasa bahas   |  |  | ٠.                       |
| Attachm nt(s) 1. ☑ Notice of References Cited (PTO-892)  | 5 D Notice of Informati  | Datas (                                      |                          |
| <ol> <li>Notice of Praftperson's Patent Drawing Review (PTO-948</li> </ol>   | inition of information of the second o | Patent Application (PTO-152)                 |                          |
|  | Paper No./Mail Da  | ate  |                          |
| <ol> <li>Information Disclosure Statements (PTO-1449 or PTO/SE<br/>Paper No./Mail Date 06/27/2003</li> </ol>   | •  | Iment/Comment                                |                          |
| 4. Examiner's Comment Regarding Requirement for Deposit  | . / )  | ent of Reasons for Allowance                 | <b>,</b>                 |
| of Biological Material   | 9, D Other   |  |                          |
|  | David Nelms  |  |                          |
|  | Supervisory Patent Examiner Technology Center 2800   |  |                          |
|  |  |  |                          |

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## **DETAILED ACTION**

## Examiner's Amendment

- 1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
- 2. In the last paragraph of each of claims 14 and 21, the phrase:

"a first bit buried in the substrate, and second bit line disposed over" has been changed to:

"the first bit line buried in the substrate, and the second bit line disposed over".

## Allowable Subject Matter

3. Claims 1-30 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach or render obvious a semiconductor device having a memory array and a method of fabricating thereof having all exclusive limitations as recited in claims 1, 8, 14, 21, and 26, comprising memory cells, transistors, and a semiconductor substrate, characterized in either:

a common source for adjacent transistors, two floating gates on opposite sides of the common source, two drains, respectively, on the other side of the two floating gates, a buried bit

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line in the substrate connecting the common sources, and a metal contact layer disposed over the substrate being aligned with and connecting the drains, or:

a word line including control electrodes for a first plurality of memory cells, a first bit line electrically connecting first current carrying electrodes for a second plurality of memory cells, a second bit line electrically connecting second current carrying electrodes for a third plurality of memory cells, the first bit line being buried in the substrate, the second bit line being over the substrate, the second bit line being vertically spaced from the first bit line and extending in parallel with the first bit line and at an elevation different from the word line.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

- 4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Copies of the references are not being furnished with this Office Action per MPEP § 707.05(a).
- U.S. Patent 5,644,532 to Chang discloses a flash EEPROM array having buried source bit lines and buried drain bit lines.

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tu-Tu Ho whose telephone number is (571) 272-1778. The examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DAVID NELMS can be reached on (571) 272-1787. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-1782.

Tu-Tu Ho May 14, 2004

David Nelms Supervisory Patent Examiner Technology Center 2800